

PATENT

Docket No. 5035-202US/29,653 USA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/518,974
Confirmation No. : 2918
Applicant : Richard Michael Taylor
Title (as amended): Microprocessor Instruction Execution Method for Exploiting
Parallelism by Time Ordering Operations in a Single Thread at
Compile Time"
Filed : December 21, 2004
Art Unit : 2183
Examiner : Huisman, David J.

Attorney Docket No. 5035-202US
S&L File No. P29,653 USA

FILED ELECTRONICALLY ON November 19, 2007

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUIRED SUBMISSION ACCOMPANYING
THE FILING OF A REQUEST FOR CONTINUED EXAMINATION (RCE)**

Sir:

This is the Required Submission accompanying the filing of a Request for Continued Examination (RCE) being filed in response to the final Office Action mailed May 17, 2007, having a period for response set to expire on August 17, 2007. A Petition extending the period for response for three months, to November 19, 2007 (November 17 being a Saturday), is included herein, along with authorization to charge the extension fee to a credit card. The following amendments and remarks are respectfully submitted.

Amendments to the claims begin on page 2 of this paper; Remarks begin on page 7 of this paper.